TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

# HN1K04FU

# High Speed Switching Applications Analog Switch Applications

Unit: mm

- High input impedance and extremely low drive current.
- $\bullet~~V_{th}$  is low and it is possible to drive directly at low-voltage CMOS.
  - $V_{th} = 0.8 \text{ to } 2.5 \text{ V}$
- Switching speed is fast.
- Suitable for high-density mounting because of a compact package.

### Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage	$V_{DS}$	50	V	
Gate-source voltage	$V_{GSS}$	10	V	
DC drain current	ID	50	mA	
Drain power dissipation	P <sub>D</sub> (Note)	200	mW	
Channel temperature	T <sub>ch</sub>	150	°C	
Storage temperature range	T <sub>stg</sub>	–55 to 150	°C	

Note: TOTAL rating

# 1. SOURCE 1 4. SOURCE 2 2. GATE 1 5. GATE 2 3. DRAIN 2 6. DRAIN 1 US6 JEDEC — JEITA — TOSHIBA 2-2J1C

Weight: 6.8 mg

# Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

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Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 0 V	_	_	1	μΑ
Drain-source breakdown voltage	V (BR) DSS	$I_D = 100 \ \mu A, \ V_{GS} = 0 \ V$	50	_	_	V
Drain cut-off current	I <sub>DSS</sub>	$V_{DS} = 50V, V_{GS} = 0 V$	_	_	1	μΑ
Gate threshold voltage	V <sub>th</sub>	$V_{DS} = 5V$ , $I_{D} = 0.1$ mA	0.8	_	2.5	V
Forward transfer admittance	Y <sub>fs</sub>	$V_{DS} = 5V$ , $I_{D} = 10 \text{ mA}$	20	_	_	mS
Drain-source ON resistance	R <sub>DS</sub> (ON)	$I_D = 10 \text{ mA}, V_{GS} = 4.0 \text{ V}$	_	20	50	Ω
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> =0 V, f = 1 MHz	_	6.3	_	pF
Reverse transfer capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> =0 V, f = 1 MHz	_	1.3	_	pF
Output capacitance	Coss	V <sub>DS</sub> = 5 V, V <sub>GS</sub> =0 V, f = 1 MHz	_	5.7	_	pF
Switching time	t <sub>on</sub>	$V_{DD} = 5 \text{ V}, I_D = 10 \text{ mA}, V_{GS} = 0 \text{ to } 4.0 \text{ V}$	_	0.11	_	
	t <sub>off</sub>	$V_{DD} = 5 \text{ V}, I_D = 10 \text{ mA}, V_{GS} = 0 \text{ to } 4.0 \text{ V}$	_	0.15	_	μS

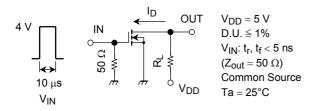
# **Equivalent Circuit (top view)**

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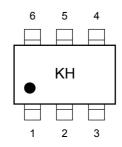
(Q1, Q2 common)

# **Switching Time Test Circuit**

## (a) Test circuit

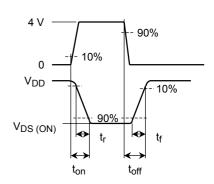


# Marking

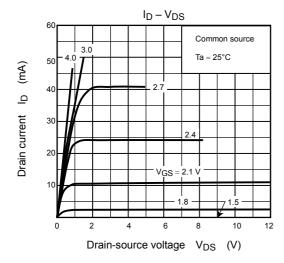


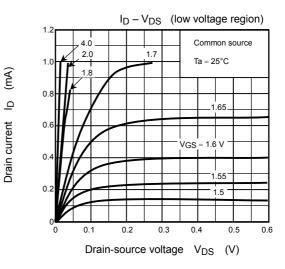
(b)  $V_{IN}$   $V_{GS}$ 

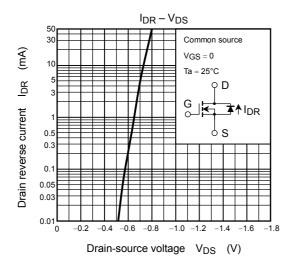
(c)  $V_{\rm OUT}$   $_{\rm V_{\rm DS}}$ 

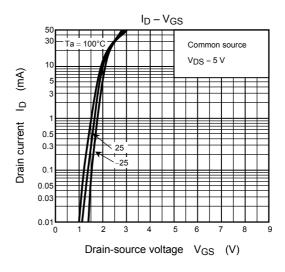


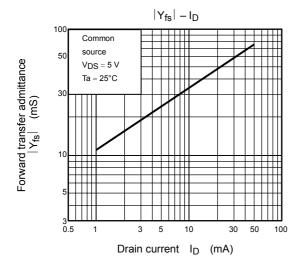
(Q1, Q2 common)

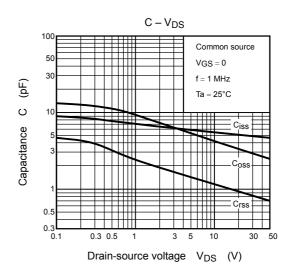






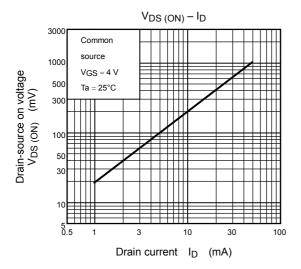


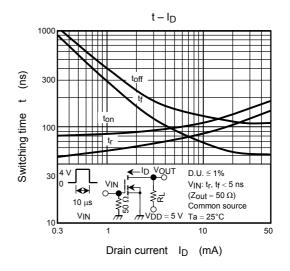


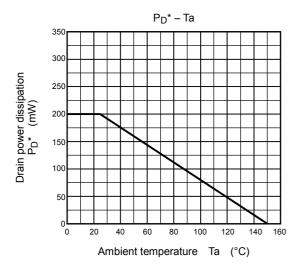


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(Q1, Q2 common)







\*: TOTAL rating

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